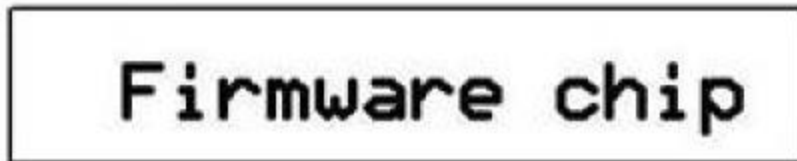
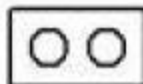


J2



J1



GND
SCK
MISO
MOSI
RST
VCC

AVR programmer board
Component side layout